

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION Fynress Mail Number <u>EV342470480US</u> <i>(Use several sheets if necessary)</i>				Docket Number (Optional) 100-23000 (P05670)		Application No. New <u>10/667,607</u>	
				Applicant(s) Douglas Robert Farrenkopf			
				Filing Date Herewith <u>9/15/2003</u>		Group Art Unit Unknown <u>2836</u>	

U.S. PATENT DOCUMENTS												
EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
RWL	6	5	5	2	8	8	6	04/22/03	Wu et al.	361	56	06/29/00
	5	5	2	1	7	8	9	05/28/96	Ohannes et al.	361	111	03/15/94
	5	3	0	1	0	8	4	08/05/94	Miller	361	91	08/21/91
	5	2	3	9	4	4	0	08/24/93	Merrill	361	91	08/14/92
	4	8	2	9	3	5	0	05/09/89	Miller	357	23.13	05/05/88

FOREIGN PATENT DOCUMENTS													
	DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	Translation	
												Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
RWL	Albert Z.H. Wang, "On-Chip ESD Protection For Integrated Circuits", An IC Design Perspective, 2002, pps. 46-47.

EXAMINER <u>Ronald M. Lepore</u>	DATE CONSIDERED <u>2/10/2005</u>
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.